

## **GCSE Computer Science Revision List**

https://isaaccomputerscience.org/topics/gcse?examBoard=all&stage=all#ocr

https://www.bbc.co.uk/bitesize/examspecs/zmtchbk

https://www.csnewbs.com/ocr-gcse

https://smartrevise.online/

## Paper 1:

- CPU registers: <u>https://student.craigndave.org/videos/ocr-gcse-j277-slr-1-1-common-cpu-components-and-their-function</u>
- CPU characteristics: https://student.craigndave.org/videos/ocr-gcse-j277-slr-1-1-the-common-characteristics-of-cpus
- Fetch, Decode and Execute cycle: <u>https://student.craigndave.org/videos/ocr-gcse-j277-slr-1-1-the-purpose-of-the-cpu-the-fetch-execute-cycle</u>
- Vonn Neumann Architecture: <u>https://student.craigndave.org/videos/ocr-gcse-j277-slr-1-1-von-neumann-architecture</u>
- Embedded systems: <u>https://student.craigndave.org/videos/ocr-gcse-j277-slr-1-1-embedded-systems</u>
- Storage: https://student.craigndave.org/videos/ocr-gcse-j277-slr-1-2-common-types-of-storage
- Cloud: https://student.craigndave.org/videos/ocr-gcse-j277-slr-1-3-the-internet
- Operating systems and Utility software: <u>https://student.craigndave.org/videos/slr1-5-systems-software</u>
- Data capacity conversions: <u>https://student.craigndave.org/videos/ocr-gcse-j277-slr-1-2-data-capacity-and-calculation-of-data-capacity-requirements</u>
- Binary shifts: <u>https://student.craigndave.org/videos/ocr-gcse-j277-slr-1-2-binary-shifts</u>
- Representation of character: <u>https://student.craigndave.org/videos/ocr-gcse-j277-slr-1-2-representing-characters-and-character-sets</u>
- Representation of image: <u>https://student.craigndave.org/videos/ocr-gcse-j277-slr-1-2-representing-images</u>
- Representation of sound: <u>https://student.craigndave.org/videos/ocr-gcse-j277-slr-1-2-representing-sound</u>
- Compression: <u>https://student.craigndave.org/videos/ocr-gcse-j277-slr-1-2-compression</u>
- LAN/WAN: <a href="https://student.craigndave.org/videos/ocr-gcse-j277-slr-1-3-types-of-networks">https://student.craigndave.org/videos/ocr-gcse-j277-slr-1-3-types-of-networks</a>



- Network hardware: https://student.craigndave.org/videos/ocr-gcse-j277-slr-1-3-hardware-to-connect-a-lan
- Topology's: https://student.craigndave.org/videos/ocr-gcse-j277-slr-1-3-star-and-mesh-network-topologies
- Forms of attack: <u>https://student.craigndave.org/videos/ocr-gcse-j277-slr-1-4-forms-of-attack</u>
- Open source vs proprietary: <u>https://student.craigndave.org/videos/ocr-gcse-j277-slr-1-6-open-source-vs-proprietary-software</u>
- Legislation: https://student.craigndave.org/videos/ocr-gcse-j277-slr-1-6-legislation-relevant-to-computer-science

## Paper 2

- High level and low level: <u>https://student.craigndave.org/videos/ocr-gcse-j277-slr-2-5-characteristics-and-purpose-of-different-levels-of-programming-language</u>
- Features of an IDE: <u>https://student.craigndave.org/videos/ocr-gcse-j277-slr-2-5-ides</u>
- Structure diagrams: https://student.craigndave.org/videos/ocr-gcse-j277-slr-2-1-structure-diagrams
- Types of errors: https://student.craigndave.org/videos/ocr-gcse-j277-slr-2-3-how-to-identify-syntax-and-logic-errors
- Searching algorithms: https://student.craigndave.org/videos/ocr-gcse-j277-slr-2-1-binary-search
- Logic gates: <u>https://student.craigndave.org/videos/slr2-4-boolean-logic</u>
- Abstraction: <u>https://student.craigndave.org/videos/ocr-gcse-j277-slr-2-1-abstraction</u>
- Defensive design: <u>https://student.craigndave.org/videos/ocr-gcse-j277-slr-2-3-defensive-design-considerations-part-1</u>
- https://student.craigndave.org/videos/ocr-gcse-j277-slr-2-3-defensive-design-considerations-part-2
- Test data/ types of tests: https://student.craigndave.org/videos/ocr-gcse-j277-slr-2-3-the-purpose-and-types-of-testing
- SQL: https://student.craigndave.org/videos/ocr-gcse-j277-slr-2-2-the-use-of-sql-to-search-for-data
- Writing of algorithms using sequence, selection, iteration and subroutines: <u>https://student.craigndave.org/videos/slr2-2-programming-fundementals</u>